

Applicant(s): Harsh D. Sharma et al.

Title: PRIORITY DELAY INSERTION CIRCUIT

Application No.: 09/925,993 Filed: August 9, 2001

Examiner: David B. Lugo Group Art Unit: 2637

Atty. Docket No.: 004-6183 Confirmation No.: 4755

May 31, 2005

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INFORMATION DISCLOSURE STATEMENT 37 C.F.R. § 1.97(c)

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Respectfully submitted,

Nicole Teitler Cave, Reg. No. 54,021

Attorney for Applicant(s)

(512) 338-6315 (direct)

(512) 338-6300 (main)

(512) 338-6301 (fax)

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PTO/SB/08A (Substitute for form 1449/PTO)	3	Attorney Docket No.:	004-6183
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INFORMATION DISCLOSURE STOEMENT B	First Named Inventor: Harsh D. Sharma		
T & TRADE		Filing Date:	August 9, 2001
		Group Art Unit:	2637
		Examiner Name:	David B. Lugo
Sheet 1 of 1		Date Submitted:	May 31, 2005

			U.S. PATENT DO	CUMENTS		
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		5,313,501A	05-17-1994	Thacker		
		US 2003/0072332 A1	04-17-2003	Tomsio et al.		
		US 2003/0120982 A1	06-26-2003	Levy et al.		
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			FOREIGN PATENT	DOCUMENTS		
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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